
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Koos et al.

Attorney Docket No.:

NOVLP068/NVLS-000818

Application No.: NEW

Examiner: UNASSIGNED

Filed: HEREWITH

Group: UNASSIGNED

Title: METHOD FOR FABRICATION OF
SEMICONDUCTOR INTERCONNECT
STRUCTURE WITH REDUCED CAPACITANCE,
LEAKAGE CURRENT, AND IMPROVED
BREAKDOWN VOLTAGE

**INFORMATION DISCLOSURE STATEMENT
37 CFR §§1.56 AND 1.97(b)**

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is: (i) filed within three (3) months of the filing date of the above-referenced application, (ii) believed to be filed before the mailing date of a first Office Action on the merits, or (iii) believed to be filed before the mailing of a first Office Action after the filing of a Request for Continued Examination under §1.114. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. NOVLP068).

Respectfully submitted,

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Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No.	Application No.:
	NOVLP068/NVLS-000818	NEW
	Applicant:	
	Koos et al.	
	Filing Date	Group
	HEREWITH	UNASSIGNED

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A1	6,342,733	1/29/02	Hu et al.			
	A2	5,486,234	1/23/96	Contolini et al.			

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	B1							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	C1	Mori et al., "Metal Capped Cu Interconnection Technology by Chemical Mechanical Polishing," VMIC Conference, 1996, 487-492
	C2	Hu et al., "Reduced Electromigration of Cu Wires by Surface Coating," Applied Physics Letters, Vol. 81, No. 10, (2002), 1782-1784
	C3	E.G. Colgan, "Selective CVD-W for Capping Damascene Cu Lines," Thin Solid Films, 262, (1995), 120-123
	C4	Enhanced Copper Metallurgy for BEOL Application, IBM Technical Disclosure Bulletin, Vol. 33, No. 5, (1990), 217-218
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.